

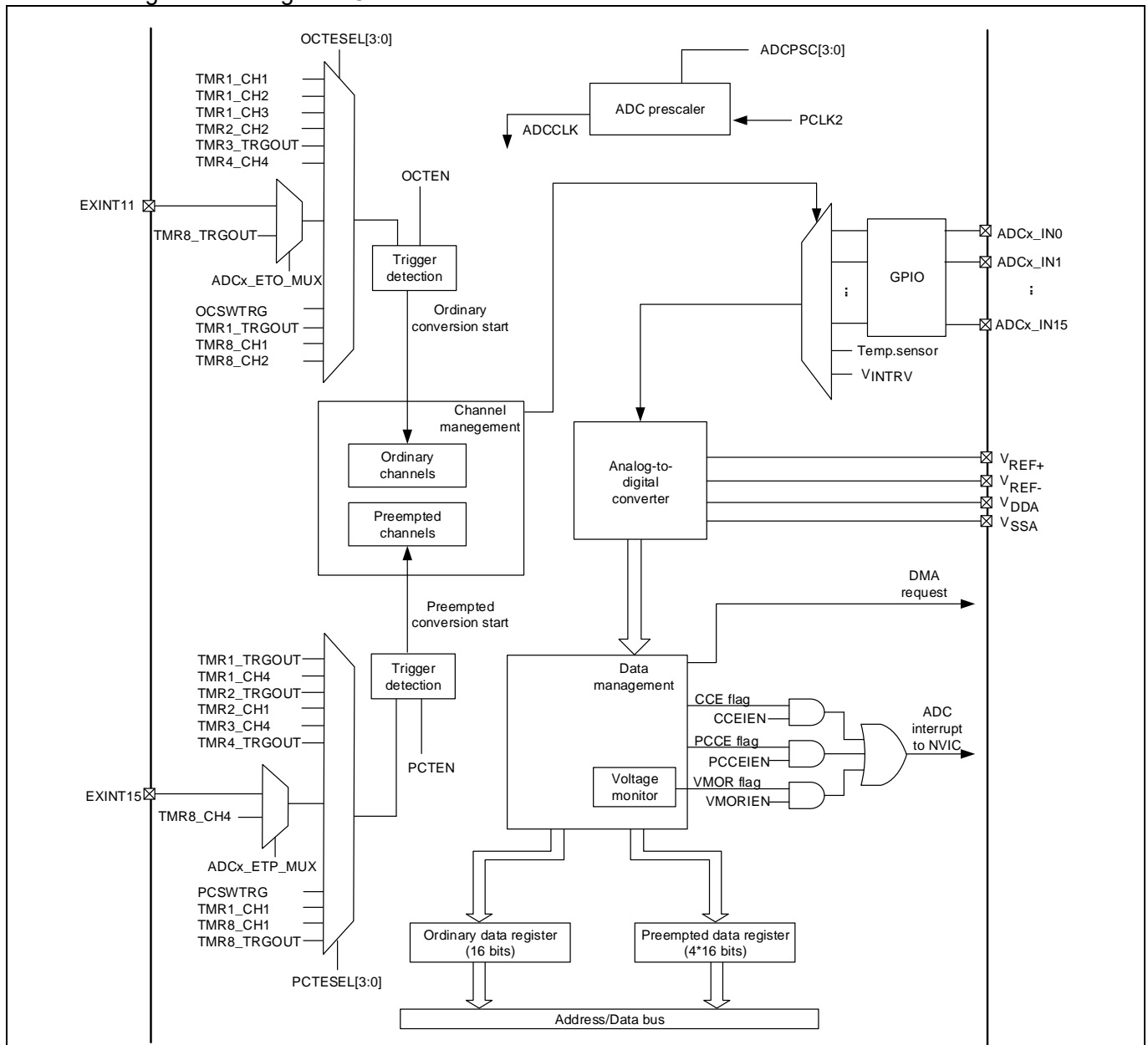
How to trigger ADC1/ADC2/ADC3 simultaneously?

Questions:

How to trigger ADC1, ADC2 and ADC3 simultaneously?

Answer:

The block diagram of a single ADC is as follows:



Based on the block diagram above, ADC supports multiple external trigger modes, and dual ADC mode (combination of ADC1 and ADC2) is supported. Therefore, by using the same trigger source in ADC3 as that in dual ADC mode (regular simultaneous mode), it is possible to achieve synchronization between ADC3 and ADC1 & ADC2. The converted data is then transmitted by DMA to the internal SRAM.

Type: MCU application

Applicable products: AT32F403, AT32F403A, AT32F407

Main function: ADC

Minor function: None

Document revision history

Date	Revision	Changes
2022.2.16	2.0.0	Initial release

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