FAQ0036
Frequently Asked Questions

Frequent enable/disable REN/TEN triggers communication failure

Questions:

Frequenct enabling/disabling REN/TEN bit during USART communication will cause RDBF bit of RX to be set for a long time even though no data is sent by PC.

Answer:

Disabling REN bit cannot happen during the process of data reception (within one byte)

For example, for half-duplex transfer, it is possible to disable REN bit as soon as the transmit operation is completed.

To avoid unwanted circumstances, the following procedures can be respected:

- Disable USART clock;
- Enalbe USART clock;
- Initialize USART but without enabling REN bit

Type: MCU applications
Applicable products: AT32F403
Main function: USART
Minor function: None

Document revision history

Date	Revision	Changes
2022.2.16	2.0.0	Initial release
2022.12.15	2.0.1	Updated descriptions

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