

RDBF unable to be cleared by writing 0

Questions:

When the USART is in regular asynchronous communication mode, the RDBF flag cannot be cleared by wrting 0 to this bit.

```
void USART2_IRQHandler(void)
{
    if(usart_flag_get(USART2, USART_RDBF_FLAG) != RESET)
    {
        /* read one byte from the receive data register */
        usart2_rx_buffer[usart2_rx_counter++] = usart_data_receive(USART2);
    }
}
```

Answer:

The RDBF flag can be cleared by reading DT register.

Type: MCU applications

Applicable products: AT32F403, AT32F413, AT32F415

Main function: USART

Minor function: None

Document revision history

Date	Revision	Changes
2022.2.16	2.0.0	Initial release

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