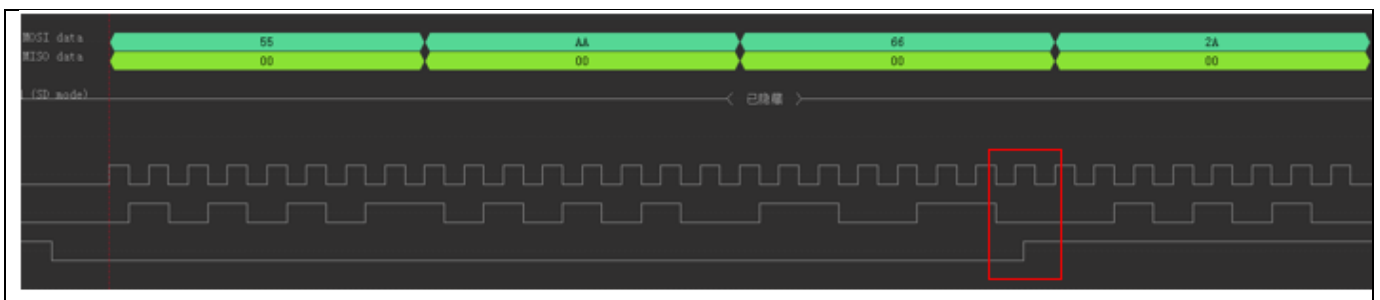


FAQ0042
Frequently Asked Questions

SPI CRC value incorrect

Questions:

When CRC calculation unit is used by SPI, if the NTC bit is written during the last half cycle, the CRC result turns out to be wrong. For example, after 0x55, 0xAA and 0x66 are sent, if NTC bit is written immediately (at the rising edge of the last cycle, marked in red below) after receiving data from 0x66, an incorrect CRC value (0x2A, which should have been 0xAA) is sent.

**Answer:**

1. Never to write NTC bit at the last half cycle
2. Use DMA transfer mode where CRC is controlled by hardware.

Type: MCU applications

Applicable products: All AT32 MCUs

Main function: SPI

Minor function: None

Document revision history

| Date | Revision | Changes |
|-------------|-----------------|-----------------|
| 2022.2.16 | 2.0.0 | Initial release |

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