FAQ0058
Frequently Asked Questions

How to disable CLKOUT output in Deepsleep mode?

Questions:

How to disable CLKOUT output in Deepsleep mode?

Answer：

For some AT32F4xx series, CLKOUT output cannot be disabled automatically in Deepsleep mode.

The solution is to disable CLKOUT output through software before Deepsleep mode entry and then configure CLKOUT after waking up from Deepsleep mode. Typical code is as follows:

```
static void clkout_config(void)
{
    gpio_init_type gpio_init_struct;
    /* enable/disable gpio port clock */
    crm_periph_clock_enable(CRM_GPIOA_PERIPH_CLOCK, TRUE);
    /* set default parameter */
    gpio_default_para_init(&gpio_init_struct);
    /* clkout gpio init */
    gpio_init_struct.gpio_drive_strength = GPIO_DRIVE_STRENGTH_STRONGER;
    gpio_init_struct.gpio_mode = GPIO_MODE_MUX;
    gpio_init_struct.gpio_pins = GPIO_PINS_8;
    gpio_init_struct.gpio_pull = GPIO_PULL_NONE;
    gpio_init(GPIOA, &gpio_init_struct);
    /* config clkout division */
    crm_clkout_div_set(CRM_CLKOUT_DIV_1);
    /* config clkout clock */
    crm_clock_out_set(CRM_CLKOUT_PLL_DIV_4);
}
```


Type: MCU applications
Applicable products: ATF32F403, AT32F413, AT32F415, ATF32F403A, ATF32F407
Main function: PWC
Minor function: CLKOUT

Document revision history

Date	Revision	Changes
2022.3.10	2.0.0	Initial release

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