FAQ0131

Frequently Asked Questions

How to erase SPIM?

Questions:

Placing SPIM erase code in a NZW area may trigger program exception or hardfault issue.

Answer:

The reason for this problem is that if there is “CPU reads main flash” operation during SPIM erase, this would make read instruction mistakenly read SPIM, resulting in data read error and even program hardfault.

Solution 1:

Disable interrupts before erasing, and then enable interrupts after the completion of erase operation. All codes during erase must be placed in a zero-wait area for execution.

Solution 2

Disable interrupts before erasing, and then enable interrupts after the completion of erase operation. All codes during erase must be placed in SRAM for execution.

Type: MCU application
Applicable products: AT32 series with SPIM feature
Main function: SPIM
Other function: None

Document revision history

Date	Revision	Changes
2022.3.30	2.0.0	Initial release

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